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Chapter 8. PCI Express

Chapter 7, "Power Management," describes the power management capability structure located in the configuration space of each PCI-PCI bridge in the PES32H8. Chapter 8, "Hot-Plug and Hot-Swap," describes the behavior of the hot-plug and hot-swap features in the PES32H8.

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Chapter 19, "Register Organization," describes the organization of all the software visible registers in the PES32NT8xG2 and provides the address space for those registers. Chapter 20, "PCI to PCI Bridge and Proprietary Port Specific Registers," lists the Type 1 configura-

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